

## Patent Assignment Assignor Details

**Assignor Name : ARDELEANU, ADRIANA**

**Total Assignments: 3**

**Assignment: 1**

<b>Reel/Frame:</b> <u>011346/0901</u>	<b>Received:</b> 12/22/2000	<b>Recorded:</b> 12/05/2000	<b>Mailed:</b> 03/01/2001	<b>Pages:</b> 7
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**Conveyance:** ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).

**Assignors**

1 <u>ARDELEANU, ADRIANA</u>	<b>Exec Dt:</b> 09/26/2000
2 <u>PAOLI, JEAN D.</u>	<b>Exec Dt:</b> 09/26/2000
3 <u>MOONEY, STEPHEN J.</u>	<b>Exec Dt:</b> 09/26/2000
4 <u>RAMAN, SURYANARAYANAN V.</u>	<b>Exec Dt:</b> 09/26/2000
5 <u>JHA, RAJESH K.</u>	<b>Exec Dt:</b> 09/26/2000
6 <u>SIKCHI, PRAKASH</u>	<b>Exec Dt:</b> 09/28/2000

**Assignee**

1 MICROSOFT CORPORATION  
ONE MICROSOFT WAY  
REDMOND, WASHINGTON 98052

**Property**

<b>App #</b>	<b>PCT #</b>	<b>Pat #</b>	<b>Pub #</b>
<u>09599813</u>	NONE	NONE	NONE

**Correspondence name and address**

LEE & HAYES, PLLC  
LANCE R. SADLER  
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SPOKANE, WA 99201

**Assignment: 2**

<b>Reel/Frame:</b> <u>013926/0902</u>	<b>Received:</b> 04/10/2003	<b>Recorded:</b> 03/28/2003	<b>Mailed:</b> 09/03/2003	<b>Pages:</b> 8
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**Conveyance:** ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).

**Assignors**

1 <u>LARCHEVEQUE, JEAN-MARIE H.</u>	<b>Exec Dt:</b> 03/27/2003
2 <u>NARENDRAN, ARUNGUNDRAM</u>	<b>Exec Dt:</b> 03/27/2003
3 <u>SIKCHI, PRAKASH</u>	<b>Exec Dt:</b> 03/27/2003
4 <u>LEVENKOV, ALEXEI</u>	<b>Exec Dt:</b> 03/27/2003
5 <u>ARDELEANU, ADRIANA</u>	<b>Exec Dt:</b> 03/27/2003
6 <u>SHUR, ANDREY</u>	<b>Exec Dt:</b> 03/27/2003
7 <u>CATORCINI, ALESSANDRO</u>	<b>Exec Dt:</b> 03/27/2003
8 <u>SELIM, NORA S.</u>	<b>Exec Dt:</b> 03/27/2003
9 <u>BATH, KAMALJIT S.</u>	<b>Exec Dt:</b> 03/27/2003

**Assignee**

1 MICROSOFT CORPORATION  
ONE MICROSOFT WAY

REDMOND, WASHINGTON 98052

**Property**

App #	PCT #	Pat #	Pub #
<u>10402640</u>	NONE	NONE	NONE

**Correspondence name and address**

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**Assignment: 3**

Reel/Frame: <u>014257/0914</u>	Received:	Recorded:	Mailed:	Pages:
	07/16/2003	06/27/2003	01/15/2004	5

**Conveyance:** ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).

**Assignors**

1 <u>PAOLI, JEAN D.</u>	<b>Exec Dt:</b> 06/26/2003
2 <u>ARDELEANU, ADRIANA</u>	<b>Exec Dt:</b> 06/24/2003
3 <u>STARK, CHRISTIAN</u>	<b>Exec Dt:</b> 06/24/2003
4 <u>RIVERS-MOORE, JONATHAN E.</u>	<b>Exec Dt:</b> 06/26/2003
5 <u>VESELOV, EVGENY N.</u>	<b>Exec Dt:</b> 06/24/2003

**Assignee**

1 MICROSOFT CORPORATION  
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 REDMOND, WASHINGTON 98052

**Property**

App #	PCT #	Pat #	Pub #
<u>10609238</u>	NONE	NONE	NONE

**Correspondence name and address**

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 SUITE 500  
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Search Results as of: 1/26/2004 12:29:35 P.M.

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 **PALM INTRANET**

## Inventor Information for 09/599813

Inventor Name	City	State/Country
ARDELEANU, ADRIANA	REDMOND	WASHINGTON
PAOLI, JEAN D.	KIRKLAND	WASHINGTON
MOONEY, STEPHEN J.	SEATTLE	WASHINGTON
RAMAN, SURYANARAYANAN V.	REDMOND	WASHINGTON
JHA, RAJESH K.	ISSAQUAH	WASHINGTON
SIKCHI, PRAKASH	ISSAQUAH	WASHINGTON

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 **PALM INTRANET****Inventor Name Search Result**

Your Search was:

Last Name = ARDELEANU

First Name = ADRIANA

Application#	Patent#	Status	Date Filed	Title	Inventor Name 4
<u>10610504</u>	Not Issued	020	06/30/2003	DECLARATIVE SOLUTION DEFINITION	ARDELEANU, ADRIANA
<u>10609238</u>	Not Issued	020	06/27/2003	MARKUP LANGUAGE EDITING WITH AN ELECTRONIC FORM	ARDELEANU, ADRIANA
<u>10402640</u>	Not Issued	030	03/28/2003	SYSTEM AND METHOD FOR REAL-TIME VALIDATION OF STRUCTURED DATA FILES	ARDELEANU, ADRIANA
<u>09599813</u>	Not Issued	030	06/21/2000	AUTHORING ARBITRARY XML DOCUMENTS USING DHTML AND XSLT	ARDELEANU, ADRIANA

**Inventor Search Completed: No Records to Display.**

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**PALM INTRANET****Inv ntor Nam Search R sult**

Your Search was:

Last Name = PAOLI

First Name = JEAN

Application#	Patent#	Status	Date Filed	Title	Inventor Name 8
<a href="#">60482577</a>	Not Issued	020	06/25/2003	CHILD'S ACTIVITY PLAY MAT	PAOLICELLI, JEANINE
<a href="#">10610504</a>	Not Issued	020	06/30/2003	DECLARATIVE SOLUTION DEFINITION	PAOLI, JEAN D.
<a href="#">10609238</a>	Not Issued	020	06/27/2003	MARKUP LANGUAGE EDITING WITH AN ELECTRONIC FORM	PAOLI, JEAN D.
<a href="#">10459179</a>	Not Issued	020	06/11/2003	BUILDING A VIEW ON MARKUP LANGUAGE DATA THROUGH A SET OF COMPONENTS	PAOLI, JEAN D.
<a href="#">10395506</a>	Not Issued	020	03/24/2003	SYSTEM AND METHOD FOR DESIGNING ELECTRONIC FORMS	PAOLI, JEAN D.
<a href="#">10395505</a>	Not Issued	020	03/24/2003	SYSTEM AND METHOD FOR DESIGNING ELECTRONIC FORMS AND HIERARCHICAL SCHEMAS	PAOLI, JEAN D.
<a href="#">09599813</a>	Not Issued	030	06/21/2000	AUTHORING ARBITRARY XML DOCUMENTS USING DHTML AND XSLT	PAOLI, JEAN D.
<a href="#">06632032</a>	<a href="#">4564982</a>	150	07/18/1984	IMPROVED CLASP FOR A BRACELET OR THE LIKE	PAOLINI , JEAN

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**Inventor Name Search Result**

Your Search was:

Last Name = MOONEY

First Name = STEPHEN

Application#	Patent#	Status	Date Filed	Title	Inventor Name 51
<u>10723188</u>	Not Issued	020	11/26/2003	MAPPING INTERACTIVE EDITS BETWEEN A VISUAL SURFACE AND STRUCTURED DATA	MOONEY, STEPHEN J.
<u>10690235</u>	Not Issued	020	10/21/2003	BIDIRECTIONAL PORT WITH CLOCK CHANNEL USED FOR SYNCHRONIZATION	MOONEY, STEPHEN R.
<u>10660415</u>	Not Issued	019	01/01/0001	CALIBRATION OF SCALE FACTOR IN ADAPTIVE EQUALIZERS	MOONEY, STEPHEN R.
<u>10660228</u>	Not Issued	019	09/10/2003	ADAPTIVE EQUALIZATION USING A CONDITIONAL UPDATE SIGN-SIGN LEAST MEAN SQUARE ALGORITHM	MOONEY, STEPHEN R.
<u>10652908</u>	Not Issued	020	08/29/2003	METHOD AND APPARATUS FOR CLOCK DESKEW	MOONEY, STEPHEN R.
<u>10632437</u>	Not Issued	020	08/01/2003	CONVERSION OF STRUCTURED DOCUMENTS	MOONEY, STEPHEN J.
<u>10632297</u>	Not Issued	020	08/01/2003	MAPPING BETWEEN STRUCTURED DATA AND A VISUAL SURFACE	MOONEY, STEPHEN J.
<u>10268865</u>	Not Issued	041	10/09/2002	DISCRETE-TIME ANALOG FILTER	MOONEY, STEPHEN R.
<u>10261574</u>	Not Issued	030	10/01/2002	PULSE AMPLITUDE MODULATED SYSTEM WITH REDUCED INTERSYMBOL INTERFERENCE	MOONEY, STEPHEN R.
<u>10225326</u>	Not	071	08/21/2002	SLEW RATE CONTROL	MOONEY,

	Issued			CIRCUIT	STEPHEN R.
<u>10132599</u>	Not Issued	041	04/25/2002	LOW JITTER EXTERNAL CLOCKING	MOONEY, STEPHEN R.
<u>10096546</u>	6639423	150	03/12/2002	CURRENT MODE DRIVER WITH VARIABLE TERMINATION	MOONEY, STEPHEN R.
<u>10062034</u>	Not Issued	030	01/31/2002	MEMORY PASS-BAND SIGNALING	MOONEY, STEPHEN R.
<u>10046448</u>	6639426	150	10/29/2001	APPARATUS FOR TESTING SIMULTANEOUS BI-DIRECTIONAL I/O CIRCUITS	MOONEY, STEPHEN R.
<u>10044087</u>	Not Issued	030	01/09/2002	REPLICA DRIVER MISMATCH CORRECTION USING A VARIABLE OFFSET COMPARATOR	MOONEY, STEPHEN R.
<u>10013165</u>	Not Issued	020	12/10/2001	LOW POWER NRZ INTERCONNECT FOR PULSED SIGNALING	MOONEY, STEPHEN R.
<u>09998008</u>	Not Issued	030	11/28/2001	SIMULTANEOUS BI-DIRECTIONAL CHANNEL SEPARATION	MOONEY, STEPHEN R.
<u>09981906</u>	Not Issued	030	10/19/2001	METHOD AND SYSTEM FOR IMPLEMENTING HOST-DEPENDENT SCSI BEHAVIOR IN A HETEROGENEOUS HOST ENVIRONMENT	MOONEY, STEPHEN
<u>09972327</u>	6597198	150	10/05/2001	CURRENT MODE BIDIRECTIONAL PORT WITH DATA CHANNEL USED FOR SYNCHRONIZATION	MOONEY, STEPHEN R.
<u>09968460</u>	Not Issued	061	09/28/2001	METHOD FOR MINIMIZING JITTER USING MATCHED, CONTROLLED-DELAY ELEMENTS SLAVED TO A CLOSED-LOOP TIMING REFERENCE	MOONEY, STEPHEN R.
<u>09967666</u>	6653893	150	09/28/2001	VOLTAGE MARGIN TESTING OF A TRANSMISSION LINE ANALOG SIGNAL USING A VARIABLE OFFSET COMPARATOR IN A DATA RECEIVER CIRCUIT	MOONEY, STEPHEN R.

<u>09960821</u>	Not Issued	030	09/21/2001	METHOD AND APPARATUS FOR OUTBOUND WAVE SUBTRACTION USING A VARIABLE OFFSET AMPLIFIER	MOONEY, STEPHEN R.
<u>09951909</u>	<u>6529037</u>	150	09/13/2001	VOLTAGE MODE BIDIRECTIONAL PORT WITH DATA CHANNEL USED FOR SYNCHRONIZATION	MOONEY, STEPHEN R.
<u>09897792</u>	<u>6501256</u>	150	06/29/2001	TRIMMABLE BANDGAP VOLTAGE REFERENCE	MOONEY, STEPHEN R.
<u>09896193</u>	<u>6628168</u>	150	06/28/2001	MULTIPLE INPUT, FULLY DIFFERENTIAL, WIDE COMMON-MODE, FOLDED-CASCODE AMPLIFIER	MOONEY, STEPHEN R.
<u>09894865</u>	Not Issued	093	06/28/2001	BIDIRECTIONAL PORT WITH CLOCK CHANNEL USED FOR SYNCHRONIZATION	MOONEY, STEPHEN R.
<u>09894486</u>	Not Issued	030	06/28/2001	CLOCK RECOVERY USING CLOCK PHASE INTERPOLATOR	MOONEY, STEPHEN R.
<u>09858346</u>	<u>6536025</u>	150	05/14/2001	RECEIVER DESKEWING OF MULTIPLE SOURCE SYNCHRONOUS BITS FROM A PARALLEL BUS	MOONEY, STEPHEN R.
<u>09835892</u>	<u>6522174</u>	150	04/16/2001	DIFFERENTIAL CASCODE CURRENT MODE DRIVER	MOONEY, STEPHEN R.
<u>09835600</u>	<u>6507225</u>	150	04/16/2001	CURRENT MODE DRIVER WITH VARIABLE EQUALIZATION	MOONEY, STEPHEN R.
<u>09797480</u>	Not Issued	092	02/28/2001	INTEGRATED CIRCUIT STUBS IN A POINT-TO-POINT SYSTEM	MOONEY, STEPHEN R.
<u>09752906</u>	<u>6437601</u>	150	12/26/2000	USING A TIMING STROBE FOR SYNCHRONIZATION AND VALIDATION IN A DIGITAL LOGIC DEVICE	MOONEY, STEPHEN R.
<u>09752895</u>	<u>6373289</u>	150	12/26/2000	DATA AND STROBE REPEATER HAVING A FREQUENCY CONTROL UNIT TO RE-TIME THE DATA AND REJECT DELAY VARIATION IN THE STROBE	MOONEY, STEPHEN R.



<u>09752883</u>	<u>6538584</u>	150	12/28/2000	TRANSITION REDUCTION ENCODER USING CURRENT AND LAST BIT SETS	MOONEY, STEPHEN R.
<u>09752880</u>	Not Issued	061	12/28/2000	ON-CHIP OBSERVABILITY BUFFER TO OBSERVER BUS TRAFFIC	MOONEY, STEPHEN R.
<u>09752647</u>	<u>6441649</u>	150	12/29/2000	RAIL-TO-RAIL INPUT CLOCKED AMPLIFIER	MOONEY, STEPHEN R.
<u>09752250</u>	<u>6456133</u>	150	12/28/2000	DUTY CYCLE CONTROL LOOP	MOONEY, STEPHEN
<u>09749061</u>	<u>6538502</u>	150	12/27/2000	HIGH BANDWIDTH SWITCHED CAPACITOR INPUT RECEIVER	MOONEY, STEPHEN R.
<u>09735858</u>	<u>6420912</u>	150	12/13/2000	VOLTAGE TO CURRENT CONVERTER	MOONEY, STEPHEN R.
<u>09694901</u>	<u>6445170</u>	150	10/24/2000	CURRENT SOURCE WITH INTERNAL VARIABLE RESISTANCE AND CONTROL LOOP FOR REDUCED PROCESS SENSITIVITY	MOONEY, STEPHEN R.
<u>09659499</u>	<u>6424175</u>	150	09/11/2000	BIASED CONTROL LOOP CIRCUIT FOR SETTING IMPEDANCE OF OUTPUT DRIVER	MOONEY, STEPHEN R.
<u>09649257</u>	<u>6377108</u>	150	08/28/2000	LOW JITTER DIFFERENTIAL AMPLIFIER WITH NEGATIVE HYSTERESIS	MOONEY, STEPHEN R.
<u>09612636</u>	Not Issued	071	07/07/2000	METHOD AND APPARATUS FOR AN ACTIVE STANDBY CONTROL SYSTEM ON A NETWORK	MOONEY, STEPHEN
<u>09609495</u>	<u>6304141</u>	150	06/30/2000	COMPLEMENTARY INPUT SELF-BIASED DIFFERENTIAL AMPLIFIER WITH GAIN COMPENSATION	MOONEY, STEPHEN R.
<u>09605624</u>	<u>6348826</u>	150	06/28/2000	DIGITAL VARIABLE-DELAY CIRCUIT HAVING VOLTAGE-MIXING INTERPOLATOR AND METHODS OF TESTING INPUT/OUTPUT BUFFERS USING SAME	MOONEY, STEPHEN R.

<a href="#">09605479</a>	<a href="#">6348811</a>	150	06/28/2000	APPARATUS AND METHODS FOR TESTING SIMULTANEOUS BI-DIRECTIONAL I/O CIRCUITS	MOONEY, STEPHEN R.
<a href="#">09605459</a>	<a href="#">6377103</a>	150	06/28/2000	SYMMETRIC, VOLTAGE-CONTROLLED CMOS DELAY CELL WITH CLOSED-LOOP REPLICIA BIAS	MOONEY, STEPHEN R.
<a href="#">09599813</a>	Not Issued	030	06/21/2000	AUTHORING ARBITRARY XML DOCUMENTS USING DHTML AND XSLT	MOONEY, STEPHEN J.
<a href="#">09584673</a>	<a href="#">6362657</a>	150	05/31/2000	SMALL APERTURE LATCH FOR USE WITH A DIFFERENTIAL CLOCK	MOONEY, STEPHEN R.
<a href="#">09584565</a>	<a href="#">6351191</a>	150	05/31/2000	DIFFERENTIAL DELAY CELL WITH COMMON DELAY CONTROL AND POWER SUPPLY	MOONEY, STEPHEN R.
<a href="#">09470686</a>	<a href="#">6453422</a>	150	12/23/1999	REFERENCE VOLTAGE DISTRIBUTION FOR MULTILOAD I/O SYSTEMS	MOONEY, STEPHEN R.

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	<input type="text" value="mooney"/>	<input type="text" value="stephen"/>	

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**Inventor Name Search Result**

Your Search was:

Last Name = RAMAN

First Name = SURYANARAYANAN

Application#	Patent#	Status	Date Filed	Title	Inventor Name 10
<u>09599813</u>	Not Issued	030	06/21/2000	AUTHORING ARBITRARY XML DOCUMENTS USING DHTML AND XSLT	RAMAN, SURYANARAYANAN V.
<u>09599812</u>	Not Issued	071	06/21/2000	ARCHITECTURES FOR AND METHODS OF PROVIDING NETWORK-BASED SOFTWARE EXTENSIONS	RAMAN, SURYANARAYANAN V.
<u>09599299</u>	Not Issued	041	06/21/2000	METHODS, SYSTEMS, ARCHITECTURES AND DATA STRUCTURES FOR DELIVERING SOFTWARE VIA A NETWORK	RAMAN, SURYANARAYANAN V.
<u>09599048</u>	Not Issued	061	06/21/2000	NETWORK-BASED SOFTWARE EXTENSIONS	RAMAN, SURYANARAYANAN V.
<u>08792301</u>	<u>6243721</u>	150	01/31/1997	METHOD AND APPARATUS FOR PROVIDING AUTOMATIC LAYOUT CAPABILITIES FOR COMPUTER FORMS	RAMAN, SURYANARAYANAN
<u>08791597</u>	<u>5999938</u>	150	01/31/1997	SYSTEM AND METHOD FOR CREATING A NEW DATA STRUCTURE IN MEMORY POPULATED WITH DATA FROM AN EXISTING DATA	RAMAN, SURYANARAYANAN

				STRUCTURE	
<u>08698862</u>	<u>5828885</u>	150	08/16/1996	METHOD AND SYSTEM FOR MERGING FILES HAVING A PARALLEL FORMAT	RAMAN , SURYANARAYANAN V.
<u>08660019</u>	<u>5818447</u>	150	06/06/1996	SYSTEM AND METHOD FOR IN-PLACE EDITING OF AN ELECTRONIC MAIL MESSAGE USING A SEPARATE PROGRAM	RAMAN , SURYANARAYANAN
<u>08436929</u>	<u>5630138</u>	150	05/08/1995	METHOD AND SYSTEM FOR MERGING FILES HAVING A PARALLEL FORMAT	RAMAN , SURYANARAYANAN V.
<u>07996705</u>	<u>5579223</u>	150	12/24/1992	METHOD AND SYSTEM FOR INCORPORATING MODIFICATIONS MADE TO A COMPUTER PROGRAM INTO A TRANSLATED VERSION OF THE COMPUTER PROGRAM	RAMAN , SURYANARAYANAN V.

Inventor Search Completed: No Records to Display.

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**Inventor Name Search Result**

Your Search was:

Last Name = JHA

First Name = RAJESH

Application#	Patent#	Status	Date Filed	Title	Inventor Name 2
<a href="#">10395490</a>	Not Issued	030	03/24/2003	SYSTEM AND METHOD FOR OFFLINE EDITING OF DATA FILES	JHA, RAJESH K.
<a href="#">09599813</a>	Not Issued	030	06/21/2000	AUTHORING ARBITRARY XML DOCUMENTS USING DHTML AND XSLT	JHA, RAJESH K.

**Inventor Search Completed:** No Records to Display.

**S arch Another: Inventor**

<b>Last Name</b>	<b>First Name</b>	<b>Search</b>
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**Inv ntor Name Search R sult**

Your Search was:

Last Name = SIKCHI

First Name = PRAKASH

Application#	Patent#	Status	Date Filed	Title	Inventor Name 8
<u>10723188</u>	Not Issued	020	11/26/2003	MAPPING INTERACTIVE EDITS BETWEEN A VISUAL SURFACE AND STRUCTURED DATA	SIKCHI, PRAKASH
<u>10632297</u>	Not Issued	020	08/01/2003	MAPPING BETWEEN STRUCTURED DATA AND A VISUAL SURFACE	SIKCHI, PRAKASH
<u>10610504</u>	Not Issued	020	06/30/2003	DECLARATIVE SOLUTION DEFINITION	SIKCHI, PRAKASH
<u>10404312</u>	Not Issued	030	03/31/2003	SYSTEM AND METHOD FOR INCREMENTALLY TRANSFORMING AND RENDERING HIERARCHICAL DATA FILES	SIKCHI, PRAKASH
<u>10402640</u>	Not Issued	030	03/28/2003	SYSTEM AND METHOD FOR REAL-TIME VALIDATION OF STRUCTURED DATA FILES	SIKCHI, PRAKASH
<u>10395490</u>	Not Issued	030	03/24/2003	SYSTEM AND METHOD FOR OFFLINE EDITING OF DATA FILES	SIKCHI, PRAKASH
<u>09599813</u>	Not Issued	030	06/21/2000	AUTHORING ARBITRARY XML DOCUMENTS USING DHTML AND XSLT	SIKCHI, PRAKASH
<u>09107212</u>	<u>6282527</u>	150	06/30/1998	ADAPTIVE PROBLEM SOLVING METHOD AND APPARATUS UTILIZING EVOLUTIONARY COMPUTATION TECHNIQUES	SIKCHI , PRAKASH

**Inventor Search Completed: No Records to Display.**

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	Last Name	First Name	
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## Continuity Information for 09/599813

**Parent Data**  
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**Child Data**  
PCT/US01/15576 is a continuation of 09599813

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